

Implementation of Multi-Modular Converter with Droop characteristics for High Speed Railway Traction System

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Abstract: Multi Modular Converters (MMC) plays a significant role in high voltage power transmission networks due to its high scalability and modularity characteristics. MMC converters produce better efficiency & show superiority in harmonic performance in high voltage applications like HVDC and high speed AC-Traction power supply systems. Its bidirectional operation also aided to get high installation reliability comparatively. The complicated power quality problems such as voltage fluctuations, Harmonic issues and reactive power problems of railway tracks due to the rapid movement of the locomotives need to be mitigated. The loss of speed is caused due to the neutral sections (NS) which are present throughout the power line. It is necessary that the neutral sections should be eradicated especially for high speed railway networks. In this paper, a new traction power transmission network which comprises zero NS and comparatively low power quality problems is designed. Droop control strategy is implemented to improve power transfer capability between the two adjacent traction substations. The implementation of MMC with parallel converter concept using the DSP processor is executed for low voltage as a prototype model. The results obtained are confirmed that the proposed system is very much suitable for high speed railway networks.

Index Terms: High speed traction, Parallel inverters, Droop characteristics, Indian traction power supply system.

I. INTRODUCTION

Public transport by electric rail is more affordable and environmentally sustainable. The electrified railway traction system is a cause of changing large nonlinear charges and polluting the electrical grid by harmonics. The balance of the phase currents and voltages is also a major problem because of the single step design of the railway interface lines network. In general, the unbalanced mechanism is a source of overloading and decreased performance [1]. Locomotives are fed by the single-phase traction transformer through catenary supply. At present Indian traction systems implement phase splitting or shifting methods for voltage balancing in each phase [2]. In phase shifting method, if the first traction substation collects power from one phase then the successive substation will collect power from another phase in sequence manner and this process repeats for each substation coming along the power line [3]. Due to this alternative phase feeding from the grid neutral sections

should be placed while the locomotive crossing the first substation to next substation to avoid high inrush currents into locomotive as shown in Figure 1. Due to the existence of neutral sections power cannot be supplied to locomotive throughout the journey [4]. To dividing the system supply from electrically isolated segments, neutral segment needs to be inserted and reduces the speed promotion of high-speed trains that affects railway safety, efficiency, and growth [5]. Technical aspects like mechanical wear and tear of the equipment, safety operation and power quality issues are increases with these neutral sections [6]. Moreover, a network imbalances still exist in the traction power supply system as loads cannot be uniformly distributed across segments [7].

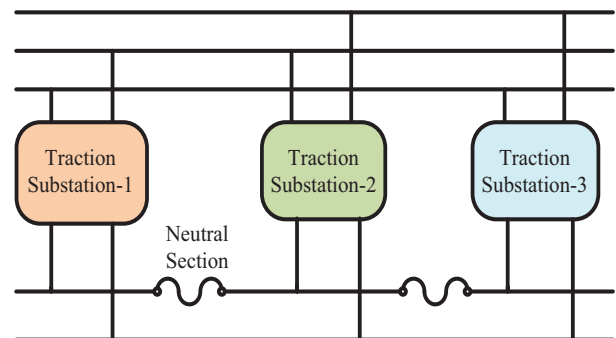


Figure 1. Indian railway phase splitting power transmission network with neutral sections

To avoid the aforementioned problems a new power delivery system is proposed which converts utility three phase grid voltage (132 KV, 50 Hz) to single phase catenary supply voltage (25KV, 50 Hz) as shown in Figure 2. Modular converters are used for the power conversion [8]. A three phase rectifier and a single phase inverter is designed with the MMC converters. There are many works handled in the MMC concept that are explained in this section which made use to identify the existing problem. A new MMC converter is implemented in [9], [10]the balancing of voltage is done in this literature. A new control with PWM modulation is implemented in [11]. The DC link control and power control is done and it is implemented inthe AC to AC conversion using MMC in [12].

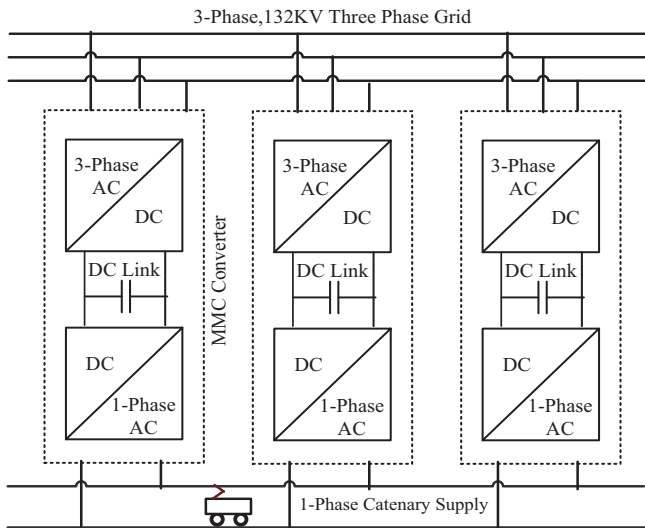


Figure 2. Proposed traction power supply network from three phase utility grid to single phase catenary with zero neutral sections.

A modular multilevel converter with huge rating is implemented as hardware in [13]. Back to back MMC with dynamic performance comparison is done in [14]. The technique of decoupling the d and q axis of ‘abc’ is presented in [15]. In this paper a new droop control is implemented with two parallel connected MMC. The hardware circuit is implemented for 110 V DC. The control technique explanation is discussed in the next section.

II. PROPOSED DROOP CONTROL METHOD

Here two adjacent substations are considered for droop control. It can be assumed one as sending end and other as

receiving end or both are sending ends. The MMC converter is connected at the substations and DC-link voltage is connected to the inverter at the load end [16]. The control of the inverter is droop control to make the synchronization working. The DC link control is common for both MMC [17, 18].

The LCL channel is utilized to constrict high recurrence exchanging clamor. Signs like the voltage and current taken from the LCL channel are changed by parks and change into direct and quadrature segments. This dq signal is utilized to ascertain the output power. A low pass channel is added after the genuine and receptive force is determined to eliminate any undesirable high recurrence signal before it is given as contribution to the hang regulator synchronization with other inverters is accomplished by contrasting the deliberate voltage and recurrence and the reference. The reference current is created from the PI regulator. This current is then contribution to other PI regulators to produce the voltage reference. A coupling inductor is utilized to couple the inverter yield. At thunderous recurrence to keep away from unsteadiness a damping resistor R_d is utilized. PLL dependent on the dq strategy is utilized to synchronize the recurrence with direct pivot. Symmetrical change is performed by utilizing the stage point structure the PLL. Every inverter is synchronized to its own nearby casing of reference. While there is a worldwidereference outline that would be reference of the substation1 while the other inverter in substation2 needs to change its nearby reference over to this in wider reference.

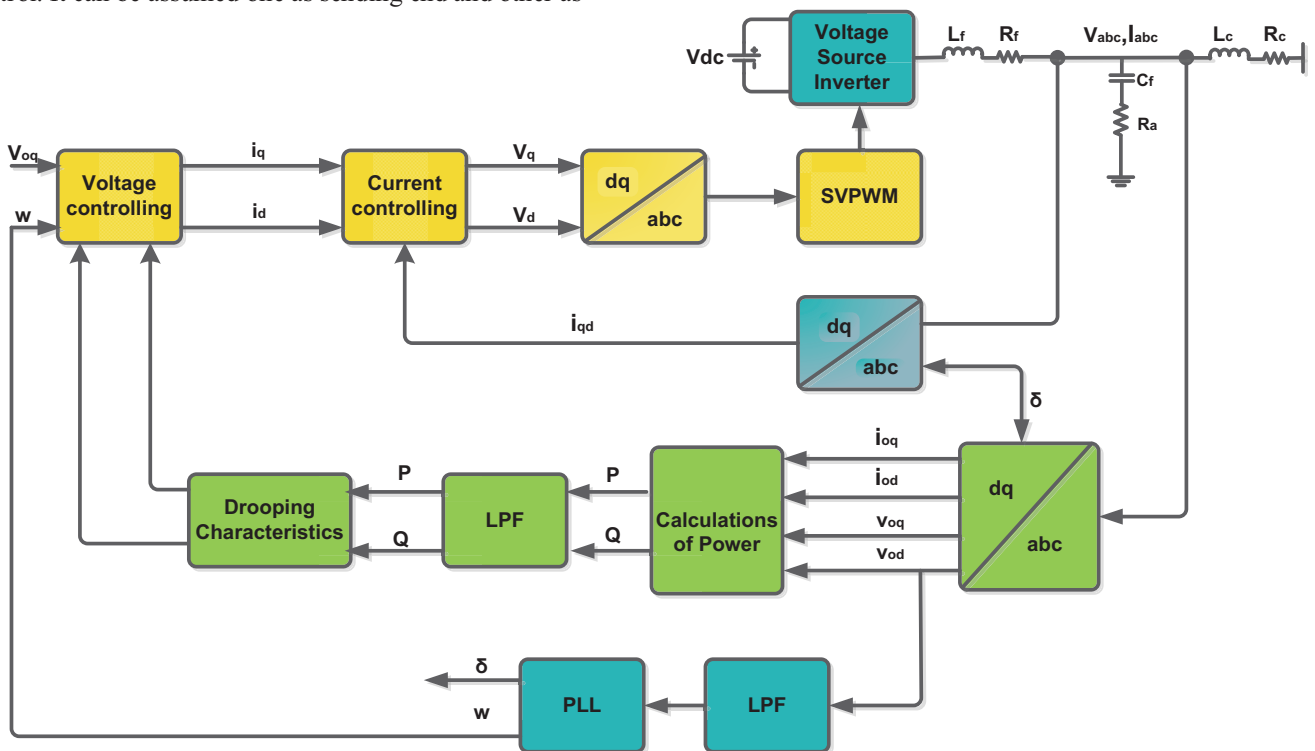


Figure 3. Self-Synchronizing Inverter with Droop Control

III. ESTIMATED VOLTAGE AND FREQUENCY

The topology of the switching module (SM) used in the MMC is that of a half bridge as shown in figure 3. The SM is ON when T1 is ON and SM is OFF when T2 is ON. Both the MOSET's are never

ON simultaneously at any point of time or else it might lead to a short circuit.

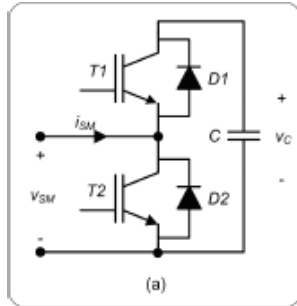


Figure 4: Switching module topology

The reference values for the angle and voltage of the inverter is set by the grid and by a PLL in the stiff grid condition. When weak grid condition occurs, the inverter has to self- synchronize using the Droop characteristics. Figure 5 (a) shows the plot of typical power and angular frequency (P- ω) curve and Figure 5 (b) shows the plot of the reactive power and voltage (Q-V) curve. The reference frequency and voltage are obtained from these curves. Thus, operating the inverters in the self-synchronizing autonomous mode.

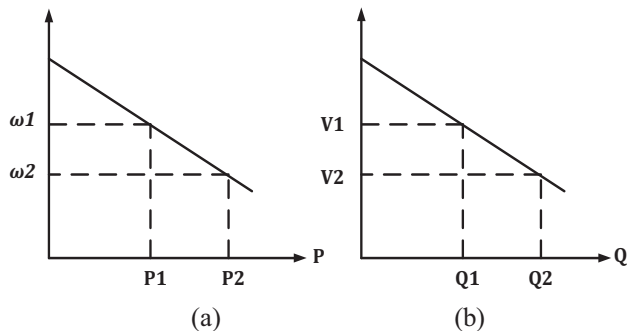


Figure 5. Droop Curves, (a) (P - ω) curve and (b) (Q - V) curve

The slopes of (P - ω) curve and (Q - V) curve is shown below,

$$m = \frac{\omega_1 - \omega_2}{P_2 - P_1} \quad \text{And} \quad n = \frac{V_1 - V_2}{Q_1 - Q_2}$$

Droop equation equations as shown below:

$$\omega^* = \omega_n - mP \dots \dots \dots (1)$$

$$v_{oq}^* = V_{oq,n} - nQ \dots \dots \dots (2)$$

The PLL is used to find the actual frequency during the autonomous condition of the AC traction system. The PLL locks into the 'd' component to zero.

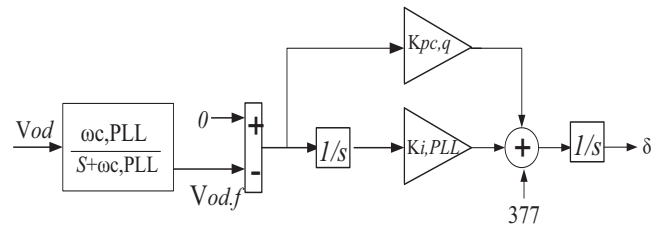


Figure 6. PLL Module.

From figure 6 the PLL is shown. It is evident that the 'd' axis component of voltage is compared with zero. This is done so to synchronize with the direct axis.

$$\dot{v}_{od,f} = \omega_{c,PLL} v_{od} - \omega_{c,PLL} v_{od,f} \dots \dots (3)$$

$$\dot{\phi}_{PLL} = -v_{od,f} \dots \dots \dots (4)$$

$$\delta \dot{} = \omega_{PLL} \dots \dots \dots (5)$$

$$\omega_{PLL} = 377 - k_{p,PLL} v_{od,f} + k_{i,PLL} \phi_{PLL} \dots \dots \dots (6)$$

The voltage controller uses the set points of voltage and frequency from the droop equations. The dq transformer components of the currents are given as input to the PI controller for the purpose of generating in the reference line current. The control variables, angular frequency from the PLL and calculated reference angular frequency is compared as shown in. Figure 7. Similarly, the other control variable 'q' component of the voltage is compared to the reference in the comparator and then given to PI controller.

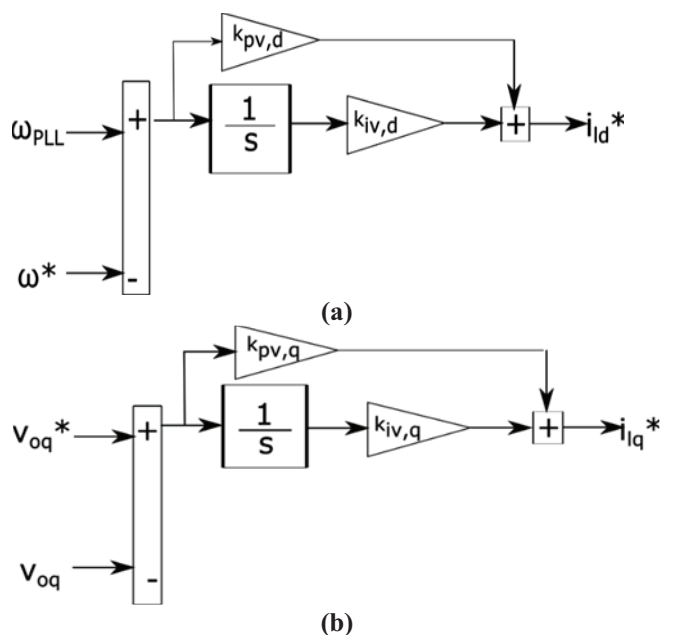


Figure 7. Voltage Controller

$$\dot{\phi}_d = \omega_{PLL} - \omega^*; \quad i_{id}^* = k_{iv,d} \phi_d + k_{pv,d} \dot{\phi}_d \dots \dots \dots (7)$$

$$\dot{\varphi}_q = v_{oq}^* - v_{oq} ;$$

$$i_{iq}^* = k_{iv,q}\varphi_q + k_{pv,q}\dot{\varphi}_q \dots \dots \dots (8)$$

$$v_{id}^* = -\omega_n L_f i_{iq} + k_{ic,d}(i_{id}^* - i_{id})$$

$$+ k_{pc,d}(i_{id}^* - i_{id}) \dots \dots (9)$$

$$v_{iq}^* = \omega_n L_f i_{id} + k_{ic,q}(i_{iq}^* - i_{iq})$$

$$+ k_{pc,q}(i_{iq}^* - i_{iq}) \dots \dots (10)$$

The reference direct and quadrature voltages are generated by the current controller. Figure 7 shows how this reference voltage generated and Figure 8 shows how this reference current generated. The control voltages v_{id}^* are calculated from the LCL filter. The PI controller is used with the current controller to get the control voltages v_{iq}^* from the command inductor currents (i_L^*). These values correspond to inverter output voltage.

The voltage commands are calculated according to the equations defined in (9) and (10).

The LCL filter consist of two inductors L_f and L_c having parasitic resistance r_f and r_c respectively. The filter capacitor C_f along is connected in parallel along with damping resistor R_d . The equation governing the filter characteristics of the LCL filter without considering the IGBT and diodes losses are as shown below,

$$i_{id}^* = \frac{1}{L_f}(-r_f i_{id} + v_{id} + v_{od}) + \omega_{PLL} i_{iq} \dots (11)$$

$$i_{iq}^* = \frac{1}{L_f}(-r_f i_{iq} + v_{iq} - v_{oq}) - \omega_{PLL} i_{id} \dots (12)$$

$$i_{od} = \frac{1}{L_c}(-r_c i_{od} + v_{od} - v_{bd}) - \omega_{PLL} i_{oq} \dots (13)$$

$$i_{oq} = \frac{1}{L_c}(-r_c i_{oq} + v_{oq} - v_{bq}) - \omega_{PLL} i_{od} \dots (14)$$

$$v_{od} = \frac{1}{C_f}(i_{id} - i_{od}) + \omega_{PLL} v_{oq}$$

$$+ R_d(i_{id} i_{od}) \dots \dots \dots (15)$$

$$v_{oq} = \frac{1}{C_f}(i_{iq} - i_{oq}) + \omega_{PLL} v_{od} +$$

$$R_d(i_{iq} i_{oq}) \dots \dots \dots (16)$$

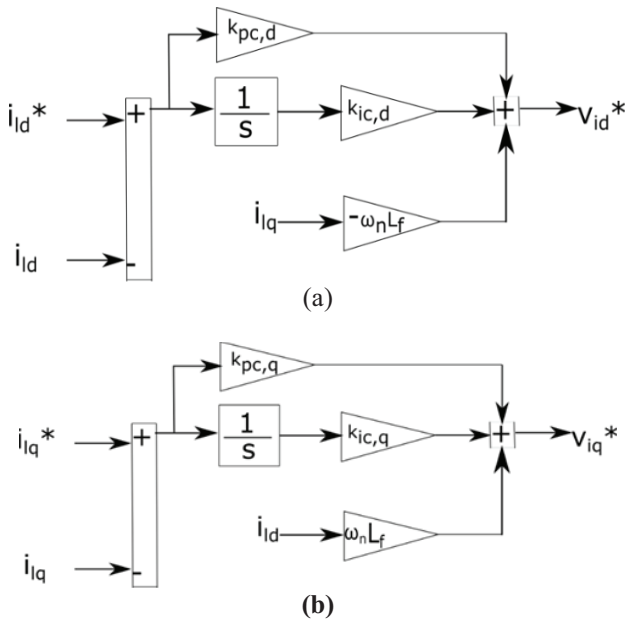


Figure 8. Current Controller

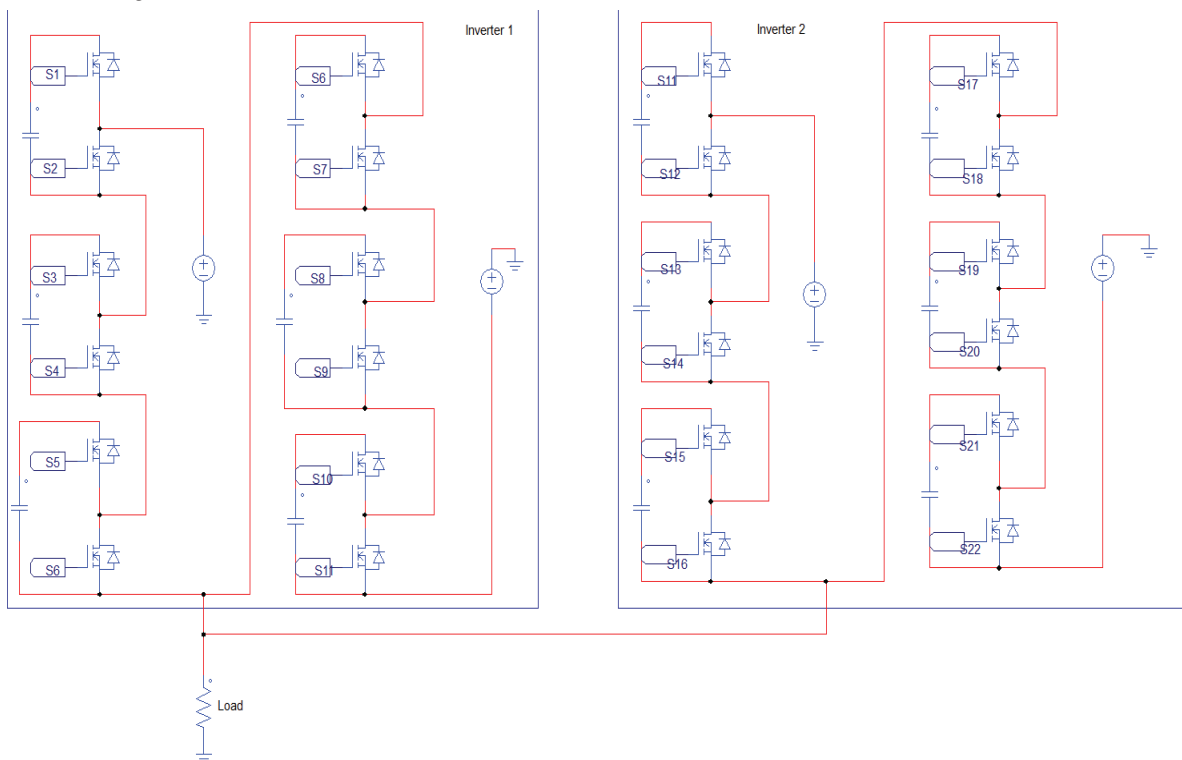


Figure 9. Design of Parallel connected MMC converter

The Figure 9 shows the design of parallel connected half bridge MMC converter with total of six MMC modules per leg so that the upper arm has three MMC and lower arms have three MMC. As the result single leg it has six MMC controllers and another inverter has the same configuration. Both are in parallel to each other.

IV. RESULTS AND DISCUSSIONS

The simulation of proposed traction substation design from three phase to single phase converter controller using a multi modular converter was built. The locomotives are taken as the loads. The basic parameters considered for the simulation are mentioned in table 1. Two parallel incerters of traction substation are balanced with droop characteristics and DC-link voltage can be controlled. To obtain the non distorted output voltage waveform LC-filter is used. The L and C parameters are designed along with the MMC.

TABLE I.
SIMULATION PARAMETERS

S.No	Parameters	Value
1	Three Phase grid voltage	132kV,50 Hz
2	Traction power rating	30MW
3	Number of traction substations considered	2
4	No of Modules per leg in MMC structure	3
5	Traction output Voltage	27.5 kV,50 Hz
6	Frequency of carrier wave	1kHz

The existing indian traction power system with the input voltage of 132KV is given as input to the traction substation and same voltage and power ratings are considered for this simulation. The Grid side voltage and current waveforms are displayed in figure 10.

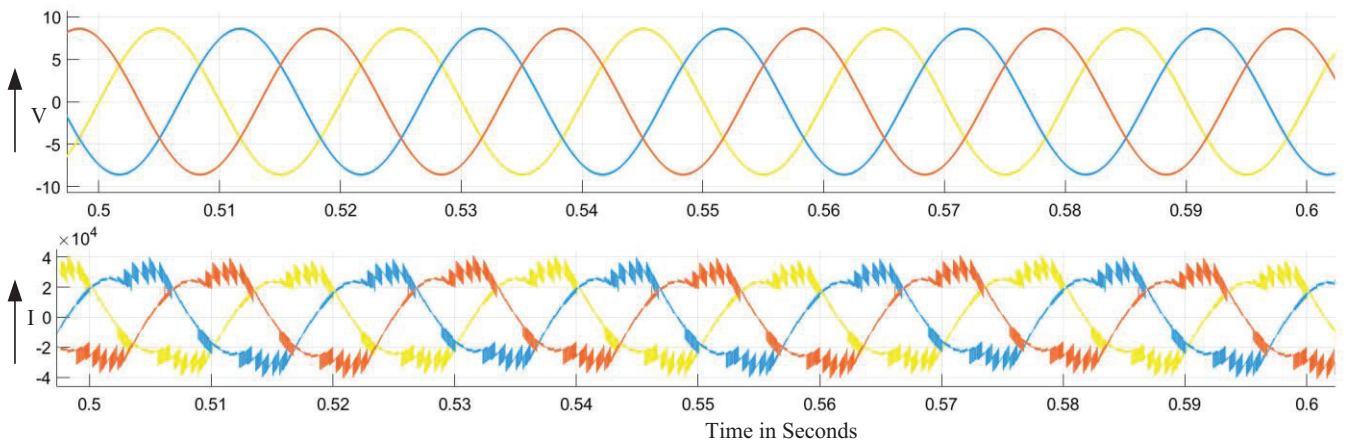


Figure 10. Three phase utility grid voltages

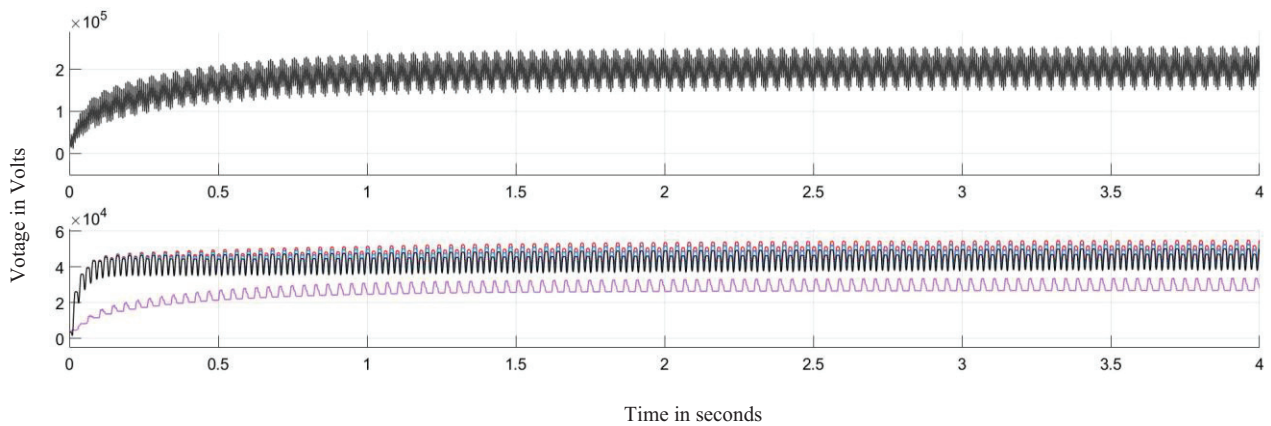


Figure 11. DC-Link Voltage waveform

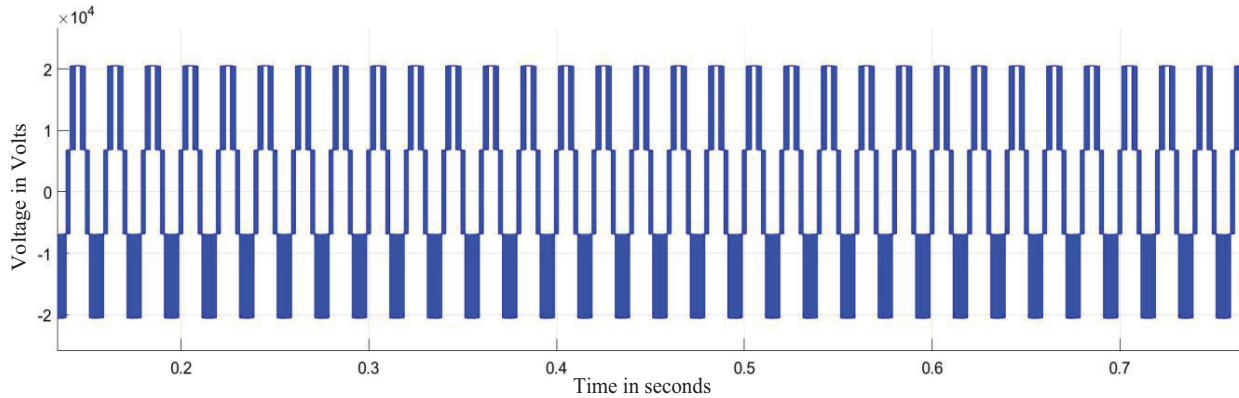


Figure 12: Single Inverter Output voltage

The DC Link capacitor has to be balanced for back back operation in MMC Converter and voltage waveform is shown in Figure 11. The output of the parallel inverter voltage is represented in the Figure 12.

V. CONCLUSIONS

The main application of the MMC is for high voltage transmission, although it has also been used in other applications such as the distribution static synchronous compensator and electrical traction. A very important application is the connection of offshore wind farms to the transmission grid. End stations can be synchronized using communication systems or work independently following specific rules. Here the droop control is implemented in the DSP processor and the results are analyzed with the input of 110V DC at the DC point. The hardware implementation of MMC for a single phase with the DSP controller is employed and the programming to the converter is done using the embedded coder in MATLAB. The procedure with circuit and outputs are explained in this paper. The results obtained for the proposed method are satisfactory.

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