

## REGISTRATION FORM

Name.....

Designation.....

Organization.....

Address for Communication.....

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Mobile Number.....

Email.....

Educational Qualifications.....

Experience .....(yrs)

### Payment Details:

Cheque : Rs.....

Cash: Rs.....

Date.....

### Any other information:

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Place:

Date:

Applicant's Signature

### Registration : Important Dates

➤Last date for receiving Applications **29<sup>th</sup> May, 2017.**

➤Date of intimation to the selected candidates through email or SMS **31<sup>st</sup> May, 2017.**

Applications are to be submitted through google link <https://goo.gl/forms/ZiDHHjMRMwnwWWQC3>. The registration fee is Rs.1200/- per participant and it can be paid by cheque /cash. For any clarification the coordinators may be contacted.

### Who can participate

The workshop will be beneficial to the faculty of Engineering institutions to conduct UG/PG VLSI laboratories and to pursue research in the field of VLSI.

### Boarding and Lodging

Tea, snacks and lunch will be provided at the venue. Participants should make their own arrangements for the lodging during their stay in Hyderabad.

### Venue – CVR College of Engineering

#### Transport:

The college operates 60 buses from various localities of the twin cities to the college campus. Participants can avail this facility.

### Contact Persons:

Mr. R.Ganesh; Ph: 09441413414

E-mail: rachaganesh@gmail.com

Mr. B. Shankar; Ph: 09989311406

E-mail: shankar.engg1577@gmail.com

Mrs. T. Subha Sri Lakshmi; Ph:9014481363

E-mail: rupashubha@gmail.com

**For more details visit the website: [www.cvr.ac.in](http://www.cvr.ac.in)**

*A One Week Short Term Course  
On*

***Digital and Analog VLSI  
Design Using Cadence Tools***

*05<sup>th</sup> - 10<sup>th</sup> June, 2017*

Chief Coordinator

**Dr. T. Esther Rani**

Professor, Dept. of ECE, CVRCE

Course Coordinators

**Mr. R Ganesh**

Assoc. Professor, Dept. of ECE, CVRCE

**Mr. B. Shankar**

Asst. Professor, Dept. of ECE, CVRCE

**Mrs. T. Subha Sri Lakshmi**

Asst. Professor, Dept. of ECE, CVRCE

Organized by



**Centre for VLSI Design**

**Department of**

**Electronics and Communications Engineering**

**CVR COLLEGE OF ENGINEERING**

**Vastunagar, Mangalpalli (V),**

**Ibrahimpatan (M), R.R.District - 501510.**

(An Autonomous Institution, Accredited by NBA, AICTE, NAAC 'A' Grade)

### About CVR College of Engineering

CVR College of Engineering was established in 2001 and its 12<sup>th</sup> batch of students graduated from the College in May 2016. The college received UGC autonomy in 2014-15 for six years. It has been accredited by the NAAC with 'A' grade for five years. The college has a clearly defined goal of evolving into a hub of academic research. As per NIRF ranking CVR is in the 101-150 group. This is the fourth best college in the Telangana state.

### About the Department

The Department of Electronics and Communication Engineering is the department that started at the time of inception of the college. The department presently offers B.Tech programme with an intake of 240 and three M. Tech programme with specializations in VLSI System Design, Embedded Systems and Wireless & Mobile Communications. The B.Tech. programme is accredited by the National Board of Accreditation (NBA) three times.

### About Centre for VLSI Design

The College has in-house capability for designing Full custom /Semi custom Chip using Cadence Tools. In addition it is strongly supported in the programmable logic domain by the Xilinx FPGA tools. The Center is currently useful to the Under Graduate and Post Graduate Programs, which enables students in better understanding of these tool driven technology and is also bridging the gap between academics and industry that needs a right cluster of well qualified faculty members.

### Patron

Dr. Cherabuddi Raghava.V, Chairman

### Chief Patron

Prof. C. Madhusudana Reddy, Advisor

Prof. K. Rama Sastri, Director

Dr. Nayanathara K. S., Prof. & Principal

Prof. L.C Siva Reddy., Vice-Principal & HoD, CSE

Dr. K. Lal Kishore, Prof. & Dean Research

Dr. N.V Rao, Prof. & Dean-Academics

Prof. P.Viswanath, HoD, ECE

Prof. C.Venkat Rao, Emirutus Professor, ECE

Prof. P.Subrahmanyam, Emitutus Professor, ECE

### Organizing Committee

Mrs. A. Anitha, Sr. Asst. Prof., ECE

Mrs.V.Silpa Kesav, Asst. Prof., ECE

Mrs. V.Ramadevi, Asst. Prof., ECE

### Technical Assistance

Mrs. R.Durga Bhavani, Acad.Asst. ECE

Mr. P.Ramesh, Acad.Asst.ECE

### Resource Persons:

1. Experts from Industry and Academia.
2. Dr. K. Lal Kishore, Prof. & Dean Research,CVRCE
3. Dr. T. Esther Rani, Prof., ECE,CVRCE
4. Mrs. K A.Jyothsna, Assoc.Prof., ECE,CVRCE
5. Mr. R.Ganesh, Assoc.Prof., ECE, CVRCE
6. Mrs.V.Silpa Kesav, Asst. Prof., ECE,CVRCE

### About the Course:

The design of digital and analog VLSI Circuits to meet non functional constraints require in depth knowledge and analysis of EDA tools. This course reviews the basic concepts of Digital and Analog VLSI circuits and provides complete guidelines for developing UG/PG VLSI projects. This course also helps in introducing the research areas to publish papers and pursue higher studies. The main objective of the course is to imbibe the knowledge of VLSI Design and hands on experience on Cadence tools to the faculty members.

### Course Contents

- ❖ Introduction to VLSI Circuits
- ❖ VLSI Design Technology Trends
- ❖ Digital VLSI Design using Cadence Tools
- ❖ Analog VLSI Design using Cadence Tools
- ❖ Power Optimization Techniques
- ❖ SPICE Models
- ❖ VLSI Research Areas

### ❖ Parallel Sessions

1. Research Topics in VLSI
2. Hands on Session in FPGA Implementation of a sample project.
3. Project Expo and Poster Presentation

### ❖ Design Project using Cadence Tools

All the participants will be given a sample project as part of course, which will help them to apply their knowledge and skills gained in the course. This will be useful to handle their research work and projects.