

REGISTRATION FORM

Name.....

Designation.....

Organization.....

Address for Communication.....

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Mobile Number.....

Email.....

Educational Qualifications.....

Experience(yrs)

Payment Details:

Cheque : Rs.....

Cash: Rs.....

Date.....

Any other information:

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Place:

Date: Applicant's Signature

Registration : Important Dates

➤Last date for receiving Applications:

15th November, 2017.

➤Date of intimation to the selected candidates through email or SMS: **15th November, 2017.**

Applications are to be submitted through google link https://docs.google.com/forms/d/1_EeC6PvXHdnpd52u1d6z-lZKm8rQJIClihyoQvbO-U/edit

The registration fee is Rs.1000/-per participant and it can be paid through cheque/ cash. For any clarification the coordinators may be contacted.

Who can participate:

The workshop will be beneficial to the Faculty, Research Scholars and Post graduate students of Engineering institutions to pursue their research in Multi-disciplinary areas using **Verilog and Matlab**. It also helps to develop projects in the field of Electronics and Communication Engineering.

Boarding and Lodging

Tea, snacks and lunch will be provided at the venue. Participants should make their own arrangements for the lodging during their stay in Hyderabad.

Venue – CVR College of Engineering

Transport:

The college operates 60 buses from various localities of the twin cities to the college campus.

Participants can avail this facility.

Contact Persons:

E-mail: ylsicvr@gmail.com

Mr. R.Ganesh; Ph: 9441413414

Mrs. T.Padmavati; Ph: 9985449294

Mrs.V.Silpa kesav; Ph: 8374199966

*A One Week Workshop
On*

***Digital System Design Using
FPGA & Simulink***

20th -25th November, 2017

Chief Coordinator

Dr. T. Esther Rani

Professor, Dept. of ECE, CVRCE

Course Coordinators

Mr. R Ganesh

Assoc. Professor, Dept. of ECE, CVRCE

Mrs. T.Padmavati

Sr. Asst. Professor, Dept. of ECE, CVRCE

Mrs. V. Silpa Kesav

Asst. Professor, Dept. of ECE, CVRCE

Organized by



Prof. Magdy Bayoumi Centre for VLSI Design

**Department of
Electronics and Communications Engineering**

CVR COLLEGE OF ENGINEERING

Vastunagar, Mangalpalli (V),

Ibrahimpatan (M), R.R.District - 501510.
(An Autonomous Institution, Accredited by NBA, AICTE, NAAC 'A')

For more details visit the website: www.cvr.ac.in

About CVR College of Engineering

CVR College of Engineering was established in 2001 and its 13th batch of students graduated from the College in May 2017. The college received UGC autonomy in 2014-15 for six years. It has been accredited by the NAAC with 'A' grade for five years. The college has a clearly defined goal of evolving into a hub of academic research. As per NIRF ranking CVRCE is in the 101-150 group.

About the Department

The Department of Electronics and Communication Engineering is the department that started at the time of inception of the college. The department presently offers B.Tech Program with an intake of 240 and three M.Tech Program with specializations in VLSI System Design, Embedded Systems and Wireless & Mobile Communications. The B.Tech Program is accredited by the National Board of Accreditation (NBA) three times.

Accomplishments of the department :

- Cadence Complete Design Suite.
- SCL IC Design Foundry library.
- UGC R&D Project worth 47.7 lakhs.
- Various Consultancy projects from DRDO, ECIL, NRSC etc. worth 67.6 lakhs.

About Centre for VLSI Design

The College has an in-house capability for designing Full custom / Semi custom Chip using Cadence Tools. In addition to that it is strongly supported by the programmable logic domain by Xilinx FPGA tools. R&D Projects related to System Generator that integrates both Xilinx and Simulink are being implemented at the center. The Center is currently useful to the Research Scholars for their research and also to the Under Graduate and Post Graduate Programs, which enables students in better understanding of these tool driven technology. This also bridges the gap between academics and industry that needs a right cluster of well qualified faculty members.

Patrons

Dr. Cherabuddi Raghava.V, Chairman
Prof. C. Madhusudan Reddy, Advisor
Advisory Committee
Prof. K. Rama Sastri, Director
Dr. Nayanathara.K. S, Prof.& Principal
Prof. L.C Siva Reddy, Vice-Principal
Dr. K. Lal Kishore, Prof & Dean Research
Dr. Rameshwara Rao, Prof.& Dean Projects
Dr. N.V Rao, Prof & Dean-Academics
Dr. K.Lalithender, Prof & HoD, ECE

Organizing Committee

Mr. P.Srinivasa Rao, Assoc. Prof., ECE
Mr.Karrar Hussain, Assoc. Prof., ECE
Mrs.K.A.Jyotsna.Assoc. Prof., ECE

Technical Assistance

Mr. B.Nagabhushan, Acad.Asst., ECE
Mr. P.Ramesh, Acad.Asst.,ECE

Resource Persons:

1. Dr. K. Lal Kishore, Prof. & Dean Research,CVRCE
2. Dr. Rameshwara Rao, Prof. & Dean Projects,CVRCE
3. Dr. K.S. Nayanathara, Prof. & Principal, CVRCE
4. Dr. T. Esther Rani, Prof., ECE,CVRCE
5. Dr. K.Yedu kondalu, Prof., ECE, CVRCE
6. Prof. CHVRS.Gopala Krishna., CVRCE
7. Mr. Narasimha, Scientist-E, RCI
8. Dr. P. Chandrasekhar, Prof., ECE, O.U
9. Mr. Srikanth, Scientist-E, DRDO
10. Mr. R.Ganesh, Assoc.Prof., ECE, CVRCE
11. Mrs.V.Silpa Kesav, Asst. Prof., ECE,CVRCE
12. Mr.Karrar Hussain, Assoc. Prof., ECE, CVRCE
13. Mr. P.Srinivasa Rao, Assoc. Prof., ECE, CVRCE
14. Mrs. T.Padmavati, Sr.Asst.Prof., ECE, CVRCE
15. Mr. D. Bhanu Prakash, Assoc.Prof., ECE, CVRCE

About the Course:

The real time system design towards System on Chip (Soc) and Internet of Things (IoT) require the knowledge of multi disciplinary areas to meet hardware and software design challenges. This workshop reviews the basic concepts of Verilog HDL for FPGA Designs, MATLAB for Simulink Designs to meet Digital VLSI and Signal Processing applications. This course also helps in introducing the research areas to publish papers and pursue higher studies.

Course Contents

- ❖ Introduction to Electronics and Communication Domains
- ❖ Design Trends in Digital System Design
- ❖ Digital System Design using Verilog HDL
- ❖ Digital Design Applications using Xilinx Tools
- ❖ Design of Signal Processing and Communications
- ❖ Design Applications using FPGA and Simulink
- ❖ Digital System Design implementation using FPGA

Design Project using FPGA/ Simulink Tools

All the participants will be given a sample project as part of workshop, which will help them to apply their knowledge and skills gained in the course.This will be useful to handle their research work and projects.