



CVR COLLEGE OF ENGINEERING

An UGC Autonomous Institution – Affiliated to JNTUH
Vastunagar, Mangalpalli (V), Ibrahimpatan (M), R.R. Dist – 501 510.

EXAMINATION BRANCH

TIME-TABLE of B.Tech. I Year II-Sem. Supple. Exams - (R12 – 2012 & 2013 ADMITTED BATCHES) – Jan, 2015

TIMINGS: 10.00 A.M to 1. 00 P.M (Forenoon)

BRANCH	05-01-2015 Monday	06-01-2015 Tuesday	07-01-2015 Wednesday	08-01-2015 Thursday	
				Engineering Physics	Engineering Chemistry
Civil Engineering	Mathematics-II <i>(Common to CE, EEE, ECE, CSE, EIE, IT)</i>	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Engineering Mechanics <i>(Common to CE, ME)</i>	2013 Admitted Batch	2012 Admitted Batch
Electrical & Electronics Engineering	Mathematics-II <i>(Common to CE, EEE, ECE, CSE, EIE, IT)</i>	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Electrical Circuits <i>(Common to EEE, ECE, EIE)</i>	2012 Admitted Batch	2013 Admitted Batch
Mechanical Engineering	Mathematics-II (Numerical Methods)	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Engineering Mechanics <i>(Common to CE, ME)</i>	-	2012 & 2013 Admitted Batches
Electronics & Communications Engineering	Mathematics-II <i>(Common to CE, EEE, ECE, CSE, EIE, IT)</i>	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Electrical Circuits <i>(Common to EEE, ECE, EIE)</i>	2012 Admitted Batch	2013 Admitted Batch
Computer Science & Engineering	Mathematics-II <i>(Common to CE, EEE, ECE, CSE, EIE, IT)</i>	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Electrical Technology <i>(Common to CSE, IT)</i>	2013 Admitted Batch	2012 Admitted Batch
Electronics & Instrumentation Engineering	Mathematics-II <i>(Common to CE, EEE, ECE, CSE, EIE, IT)</i>	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Electrical Circuits <i>(Common to EEE, ECE, EIE)</i>	2012 & 2013 Admitted Batches	-
Information Technology	Mathematics-II <i>(Common to CE, EEE, ECE, CSE, EIE, IT)</i>	Data Structures through 'C' <i>(Common to CE, EEE, ME, ECE, CSE, EIE, IT)</i>	Electrical Technology <i>(Common to CSE, IT)</i>	2013 Admitted Batch	2012 Admitted Batch

- NOTE:** (i) ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY.
(ii) EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS USUAL.

Date : 02-12-2014

PRINCIPAL

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EXAMINATION BRANCH

TIME-TABLE of B.Tech. I Year II Sem. Supple. Exams - (R12 – 2012 & 2013 ADMITTED BATCHES) – Jan, 2015

ENGINEERING DRAWING – II → COMMON TO CE, EEE, ME, ECE, CSE, EIE, IT

DATE OF EXAM: 09-01-2015

DAY: FRIDAY

EXAM TIME : 10.00 AM TO 1:00 PM

ENGINEERING DRAWING - II

CIVIL ENGINEERING

(01-C E)

ELECTRICAL AND ELECTRONICS ENGINEERING

(02-E E E),

MECHANICAL ENGINEERING

(03 - ME)

ELECTRONICS AND COMMN. ENGINEERING

(04-E C E)

COMPUTER SCIENCE AND ENGINEERING

(05-C S E)

ELECTRONICS AND INSTRUMENTATION ENGINEERING

(10-E I E)

INFORMATION TECHNOLOGY

(12- I T)

NOTE : 1) ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

Date :02-12-2014

PRINCIPAL